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Co-coordinator: 1. Mrs. T. Shanthi, Assistant Professor, Dept. of ECE MITS.

Total Participants: 64-Students and 2-Faculties.

Venue: Siemens Computer Lab (EB:019)

The APSSDC- Skill Development Cell, Madanapalle Institute of Technology and Science, Andhra Pradesh, Madanapalle in association with the Department of Electronics & Communication Engineering, MITS has organized a **six-day skill development program** from 19.02.2023 to 24.02.2024 on **"PCB DESIGN"** In this program, about 64 participants participated and made the event grant success.



During Inauguration

A summary of the skill development program is as follows:

Dr. P. Ramanathan, Professor & Vice Principal of academics, MITS, and Dr. S. Rajasekaran, Professor & Head of the Dept., Dept. of Electronics & Communication Engineering, welcomed the resource person. Dr. S. Rajasekaran, HOD / Electronics & Communication Engineering, gave a brief introduction to the six-day skill development program. Dr. P. Ramanathan, Professor and Vice Principal-Academics, inaugurated the program with his motivational speech. Dr. V B Thurai Raaj, Assistant Professor in EEE & SPOC-APSSDC t-SDI, introduced the resource person and he handed over the session to the resource person.

The 64 students from the second year and two faculties from the Department of Electronics & Communication Engineering participated in this six-day hands-on training program.

Day-1(19.02.2024)

Morning session: Design of doorbell circuit

Afternoon session: LDR sensor
Day-2(20.02.2024)
Morning session: To control the brightness of the LED
Afternoon session: 555 Timer
Day-3(21.02.2024)
Morning session: Fire alarm
Afternoon session: Clap circuit
Day-4(22.02.2024)
Morning session: Water level indicator
Afternoon session: LED chaser circuit
Day-5(23.02.2024)
Morning session: Detector circuit
Afternoon session: Motion sensor
Day-6(24.02.2024)
Morning session: Solar tracker
Afternoon session: Motor speed controller.

Outcomes: Students can be able to

- 1. Learn schematic capture
- 2. Create a PCB layout
- 3. Synchronize schematics to PCB board
- 4. Design a PCB stack-up



During Training Sessions

I would like to thank the management and our principal, Dr. C. Yuvaraj, for providing the authorization needed to conduct this program together. I thank for the timely provision of the requirements and the help provided by Dr. C. Kamal Basha, Professor and Vice Principal-Administration. I express my gratitude to Professor Dr. S. Rajasekaran, HOD/ECE, for his unwavering mentoring in all areas. Finally, I would like to express my gratitude to Dr. S. Rajasekaran, HOD/ECE for the opportunity to run this program.